

Figure 1

102 ↗

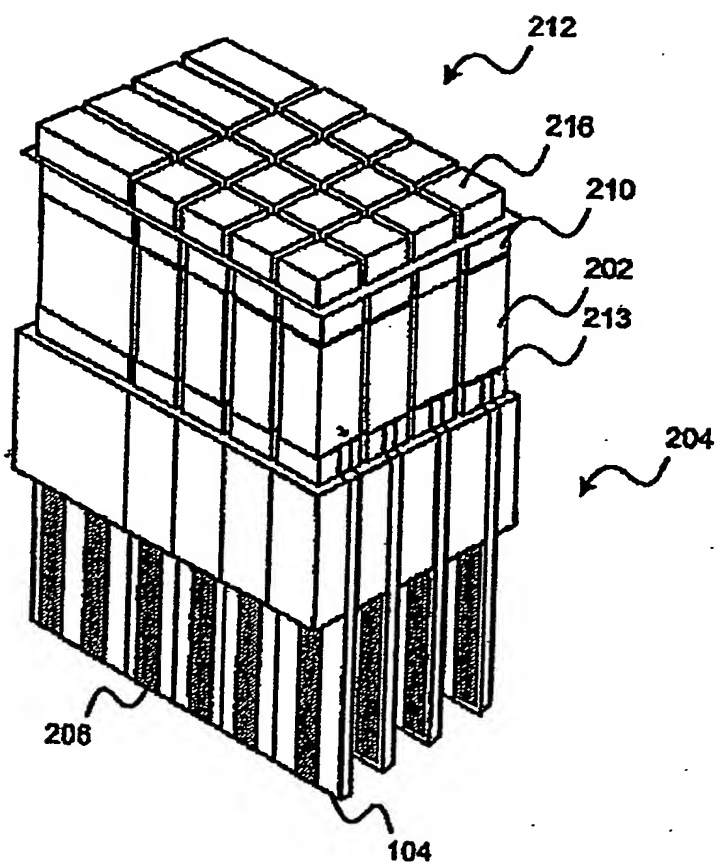


Figure 2

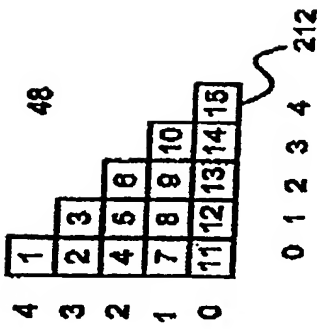
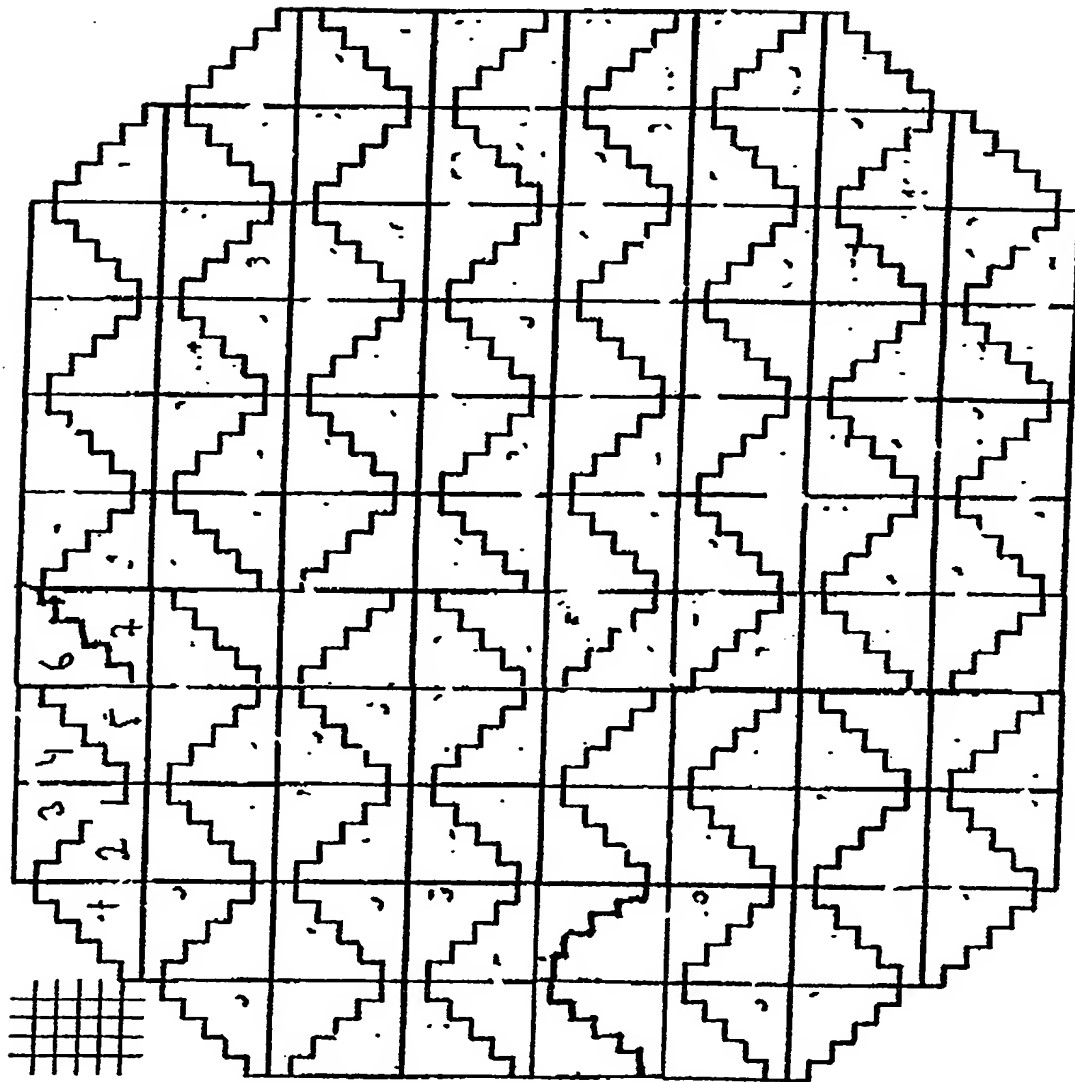


Figure 3

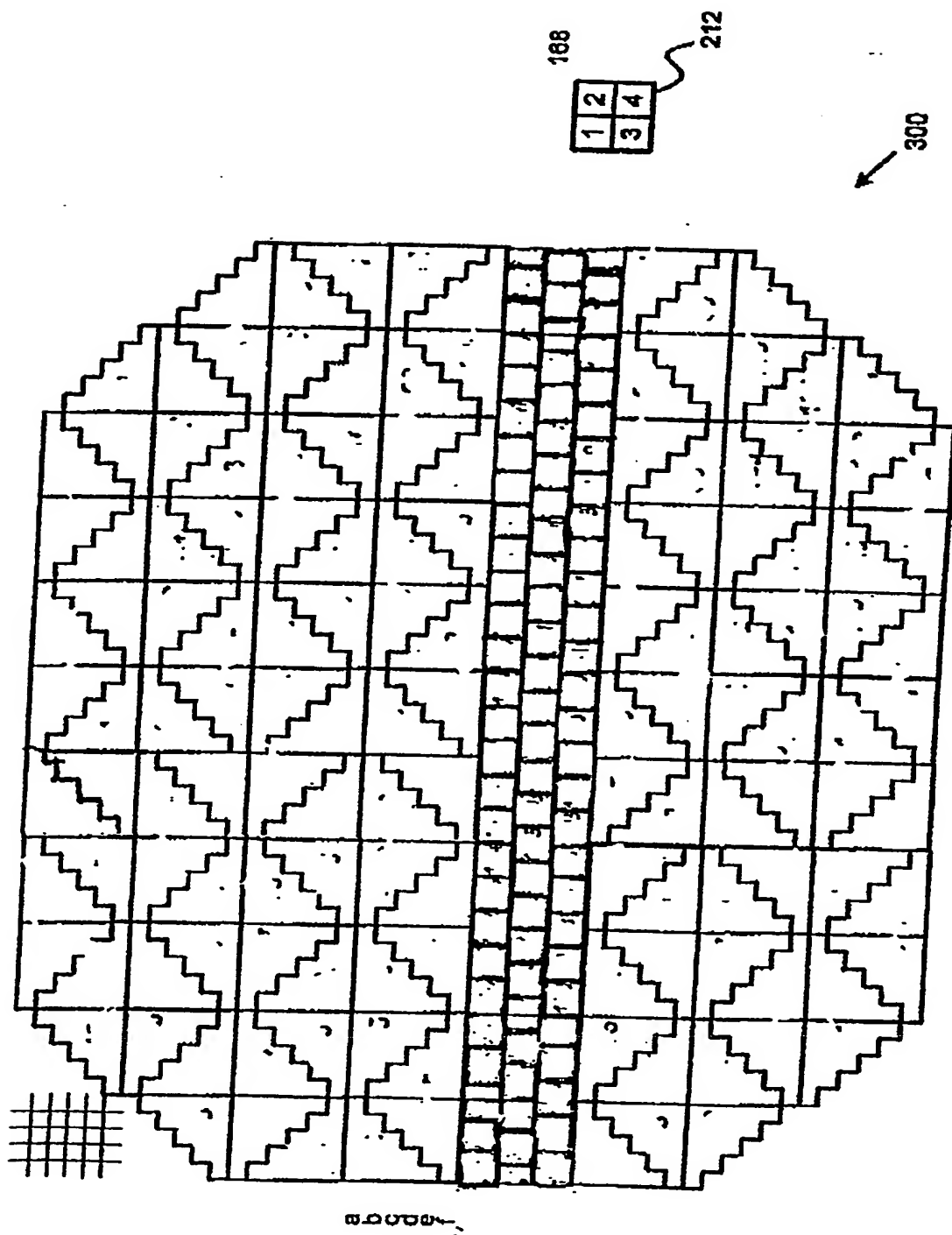


Figure 4

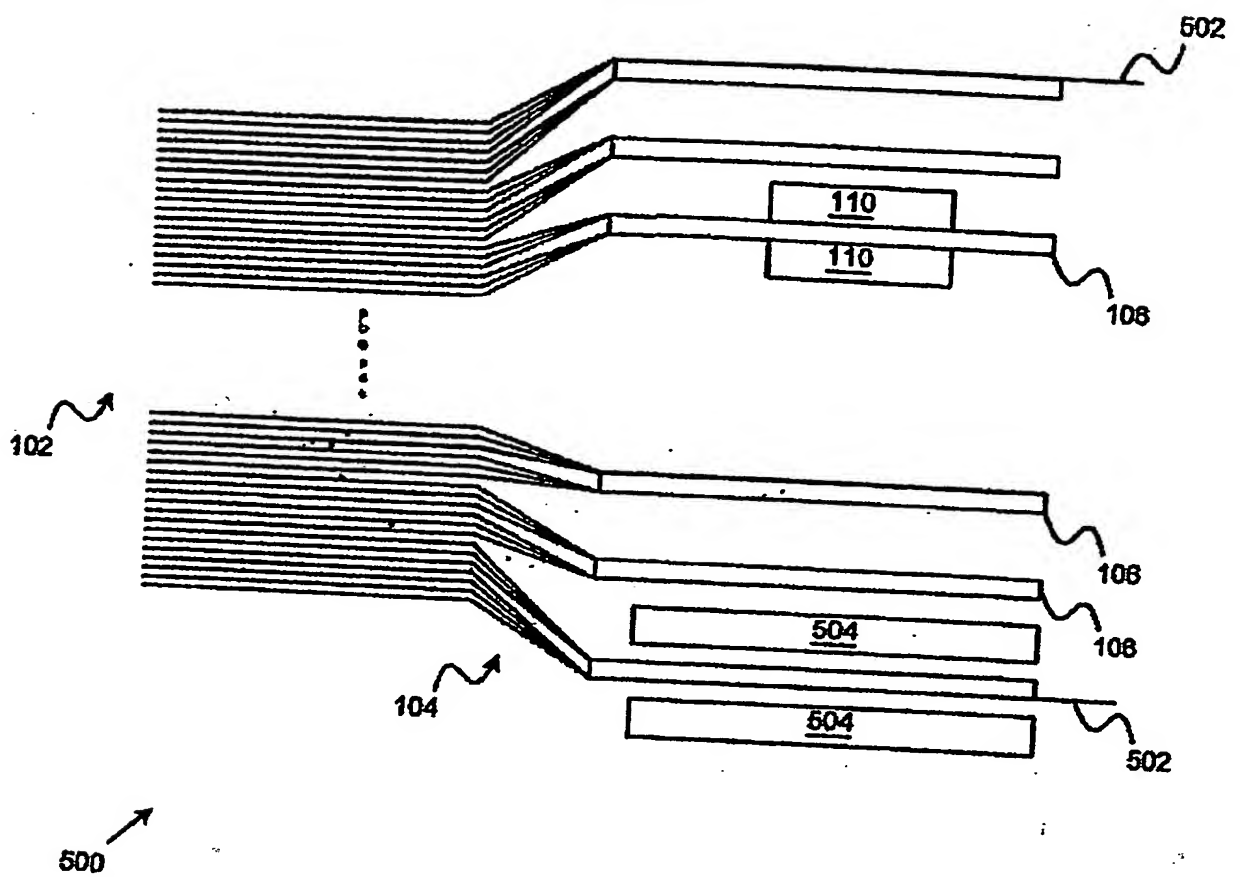


Figure 5

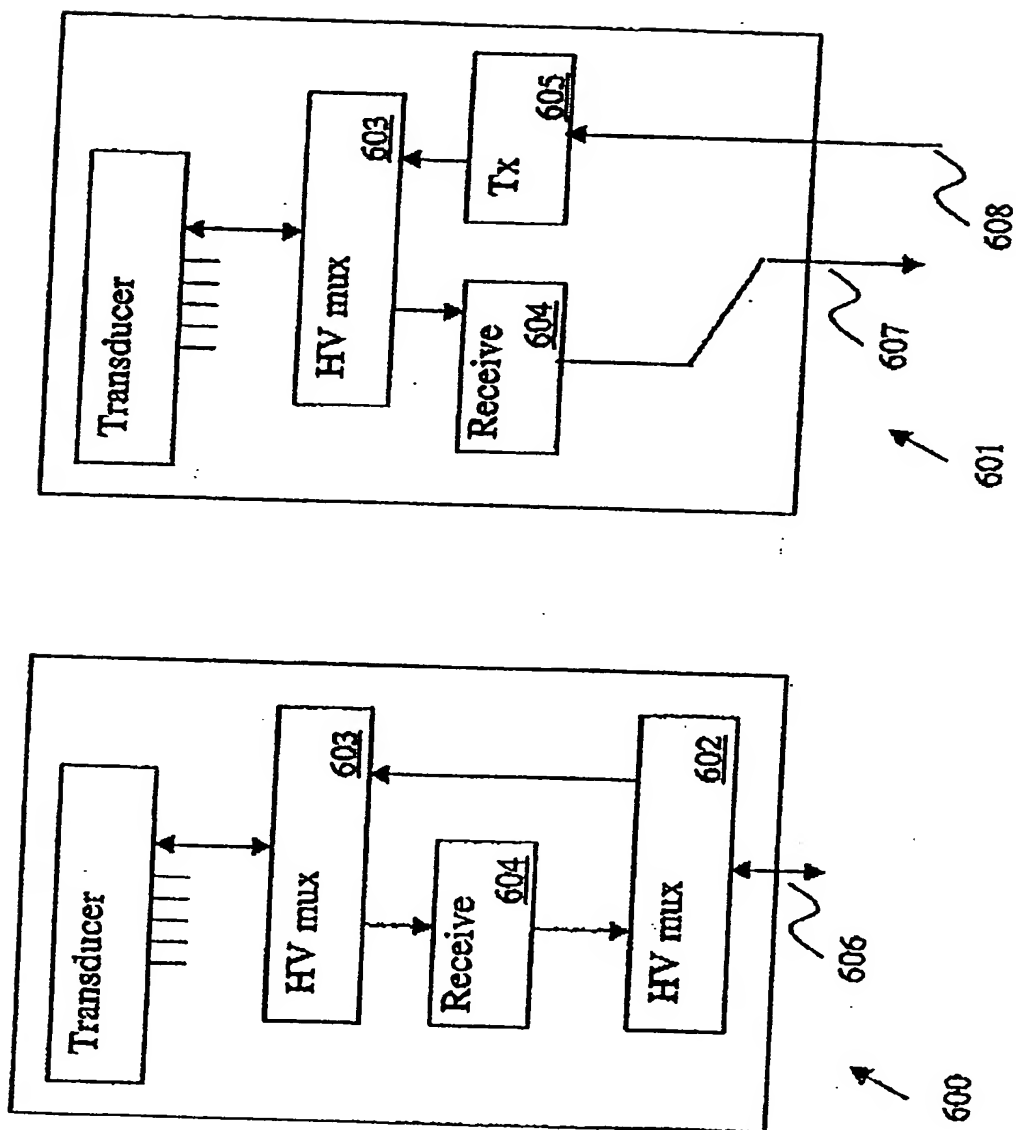


Figure 6

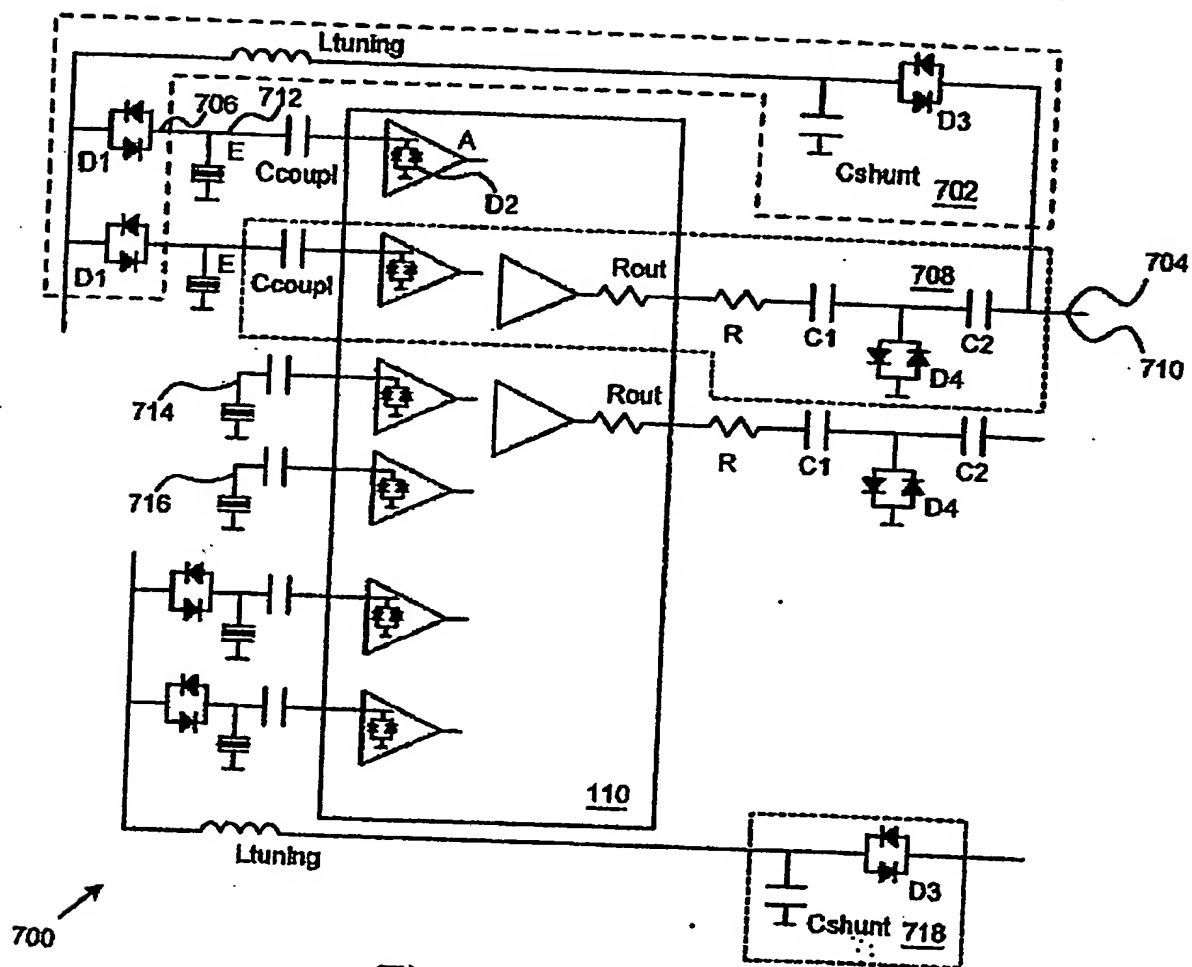
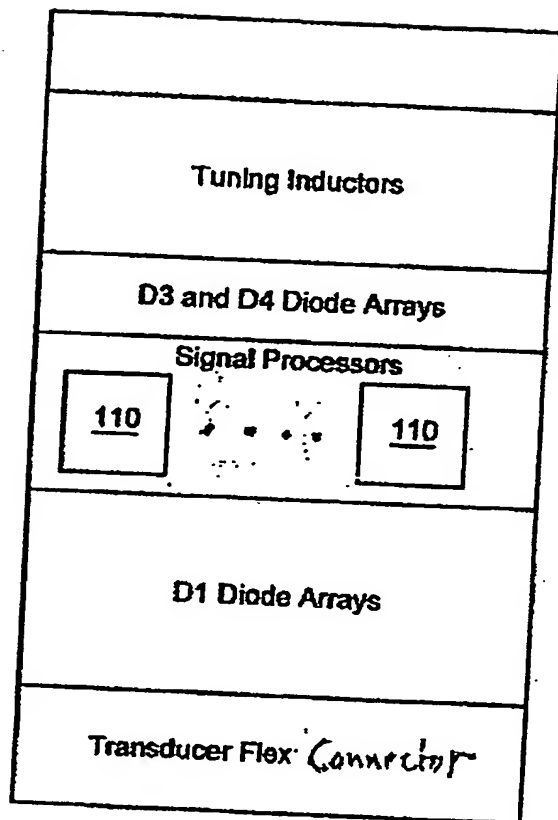
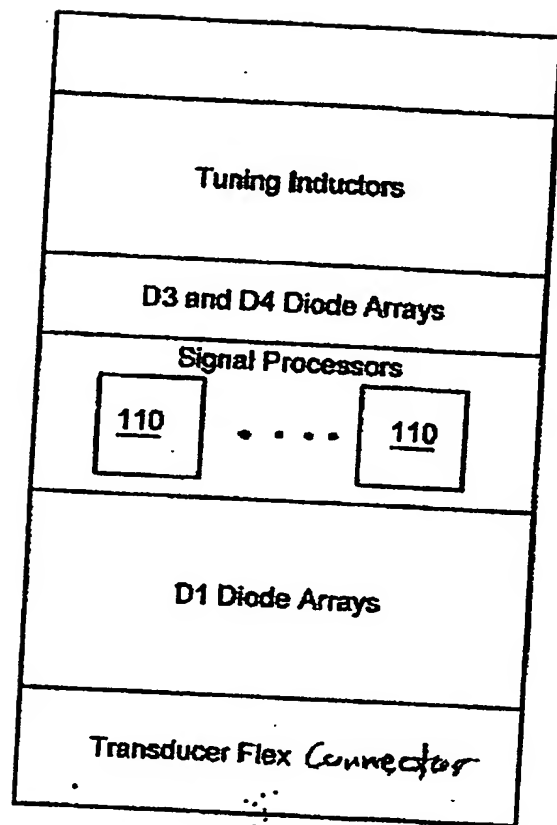


Figure 7

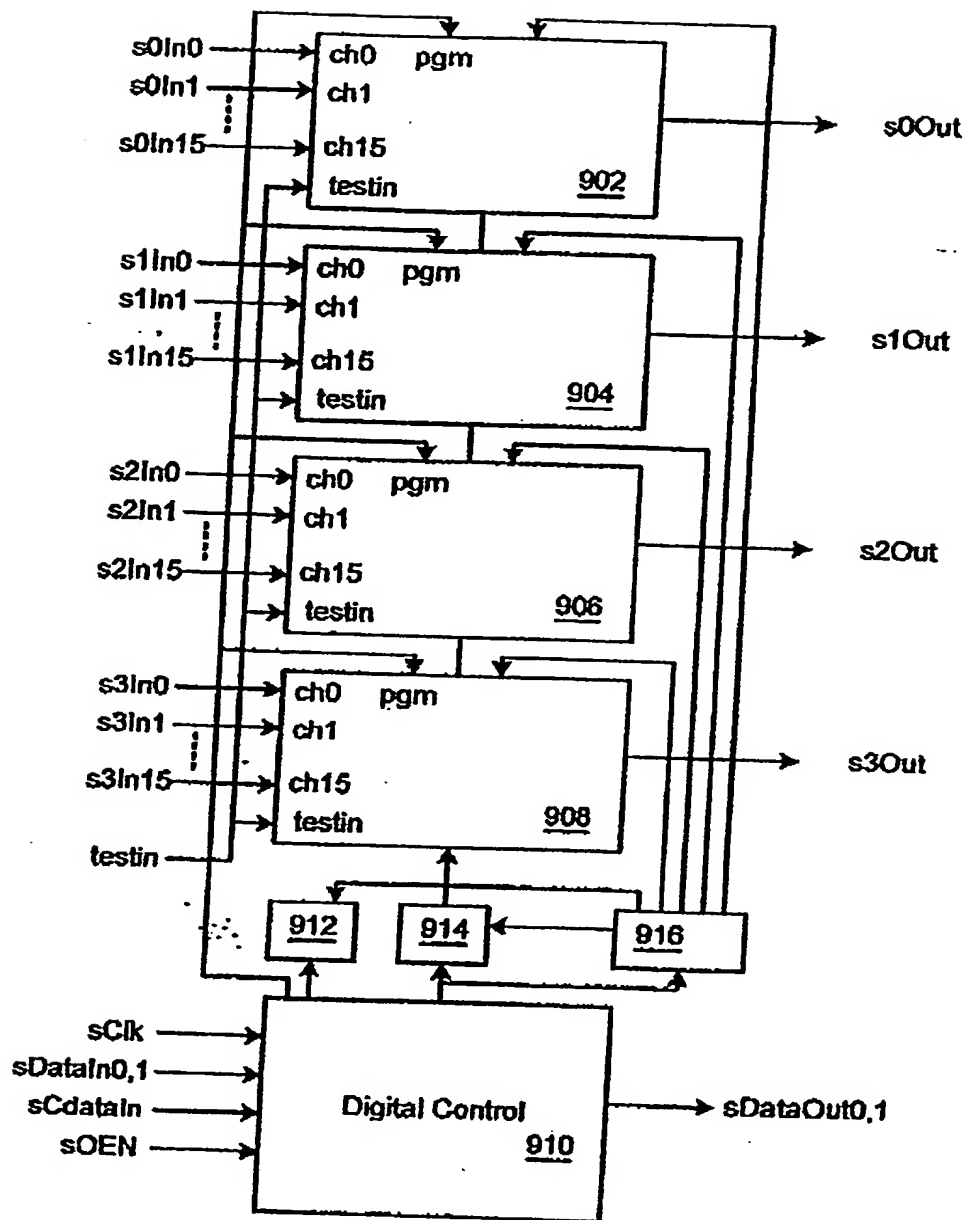


802



804

Figure 8



900

Figure 9

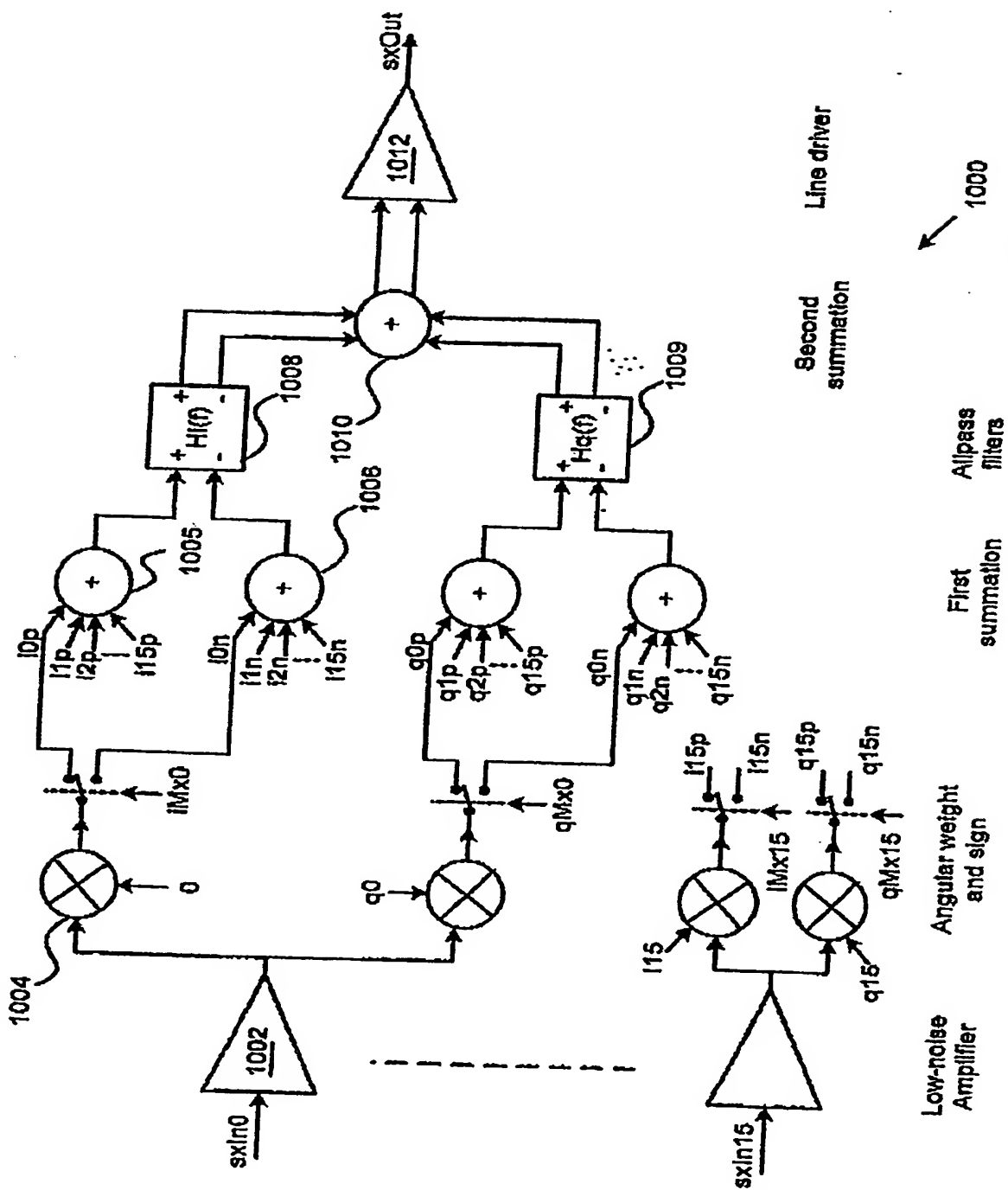
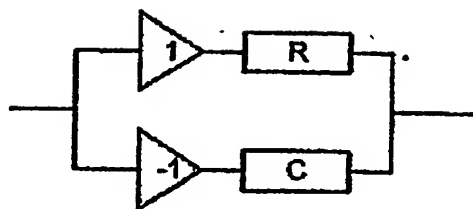


Figure 10



1906 ↗

Figure 11

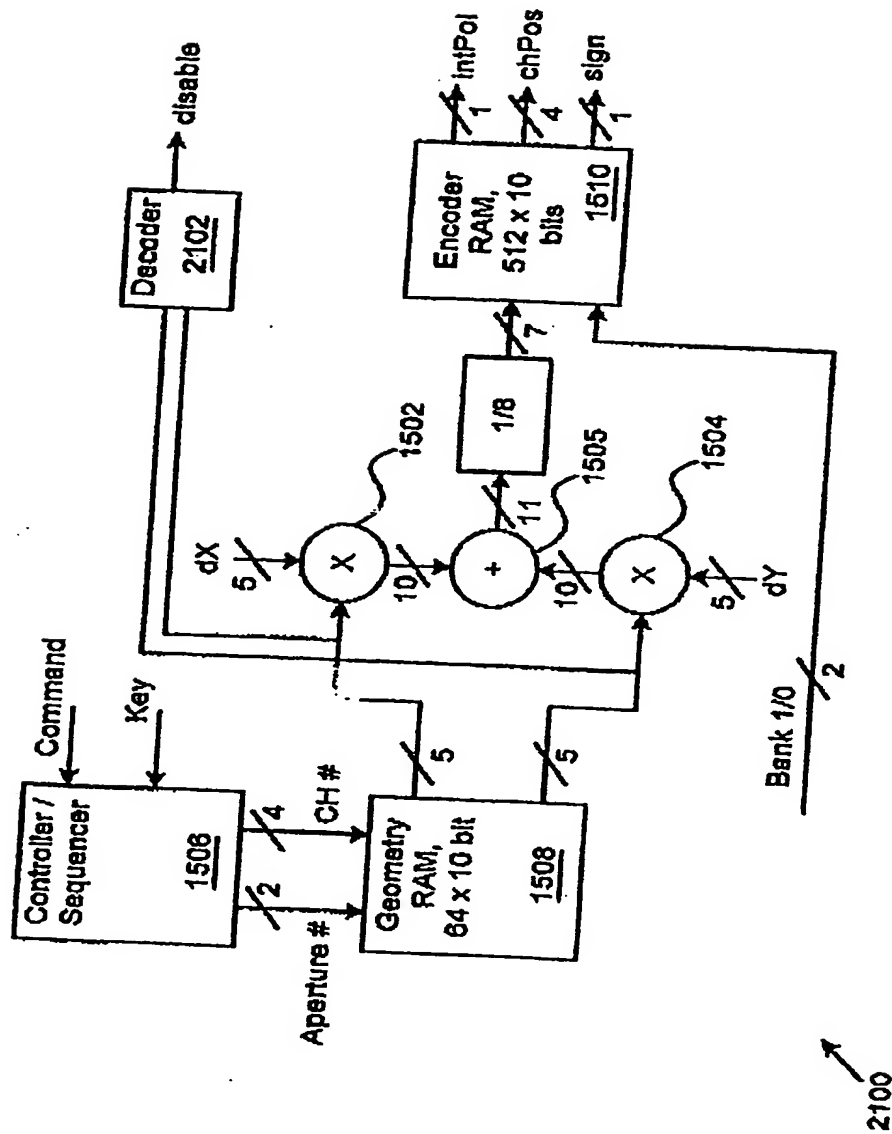


Figure 12

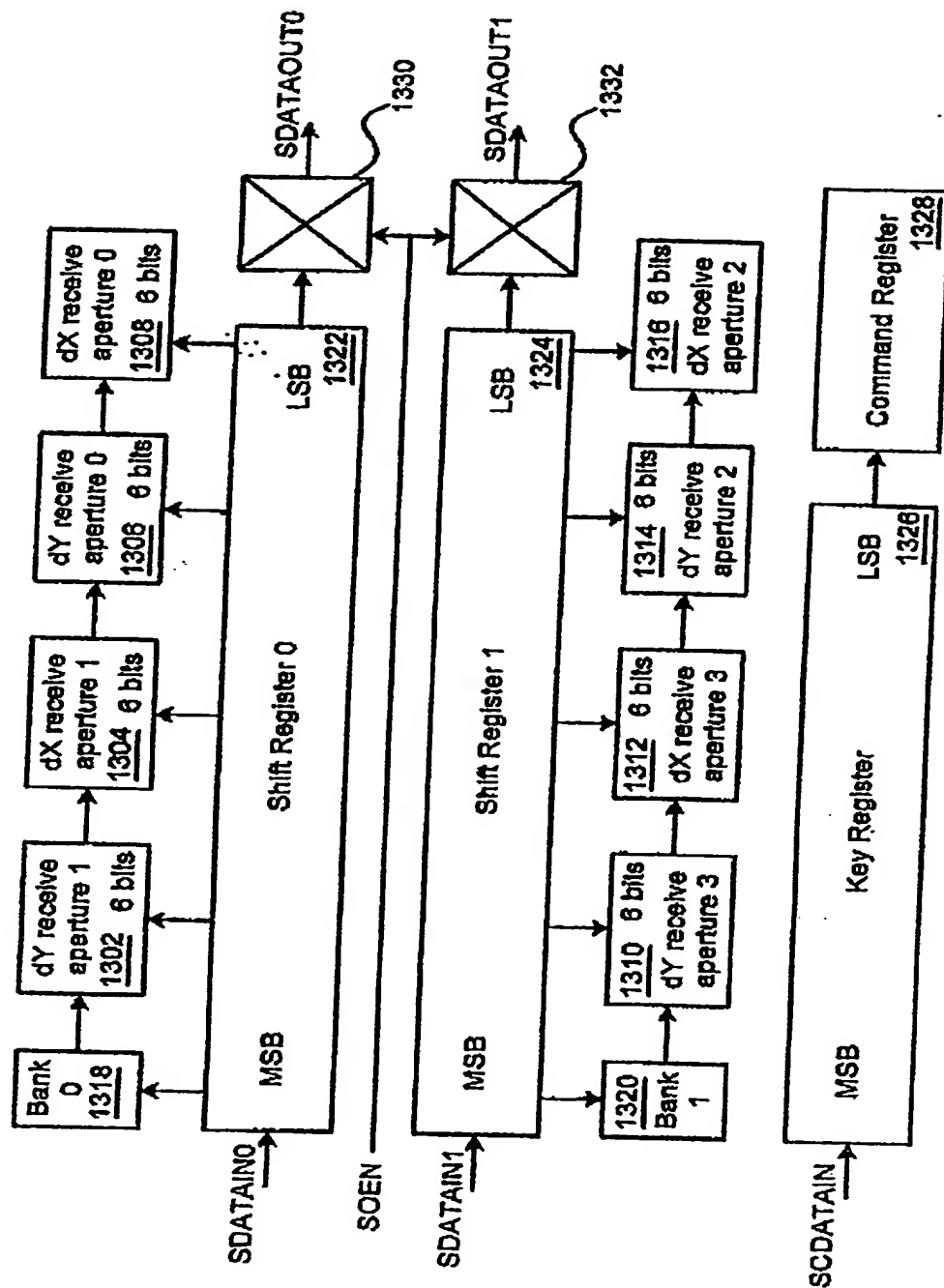


Figure 13

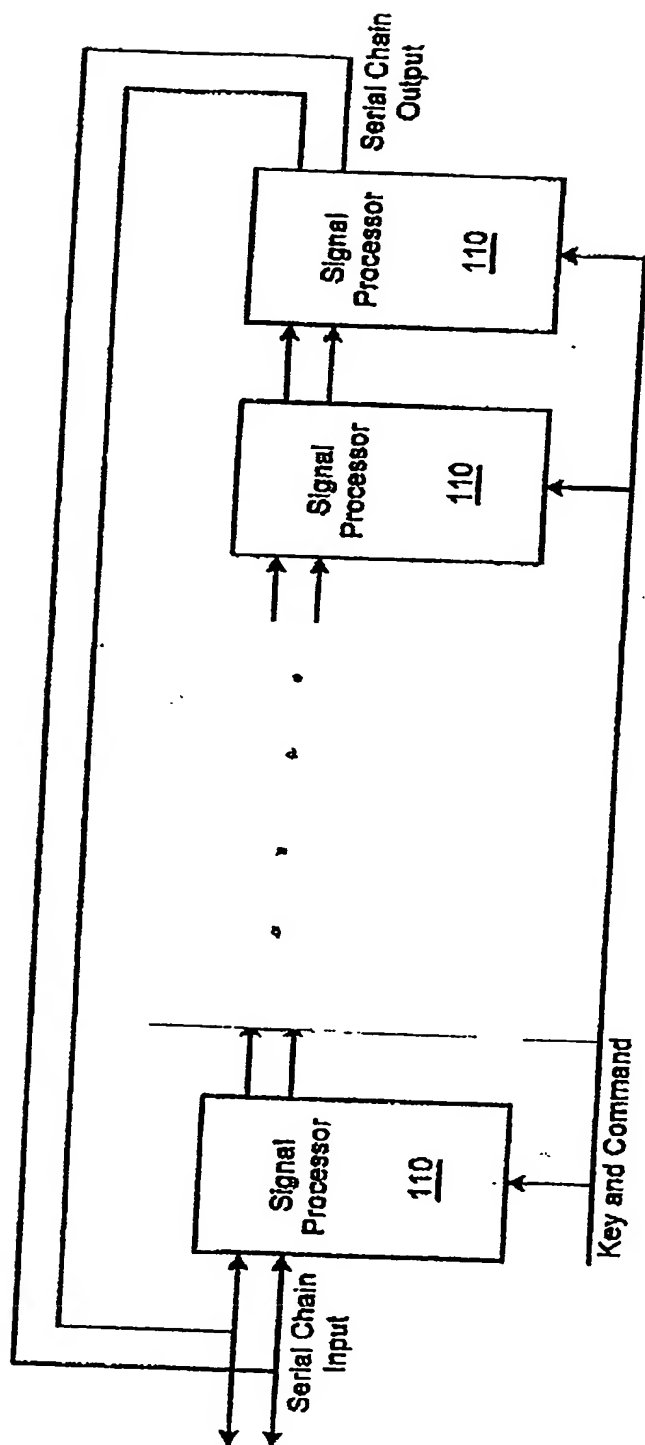


Figure 14

1400

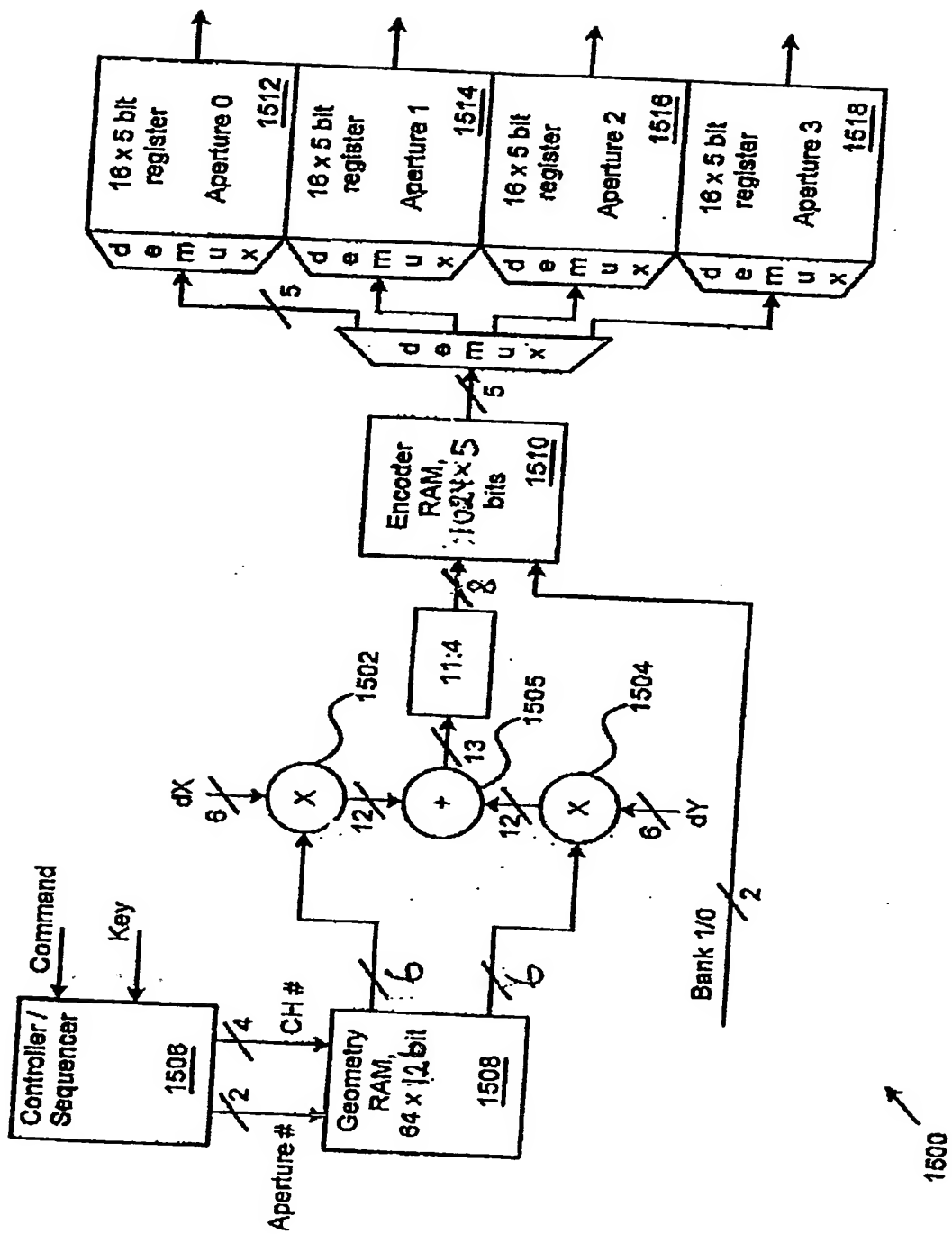


Figure 15

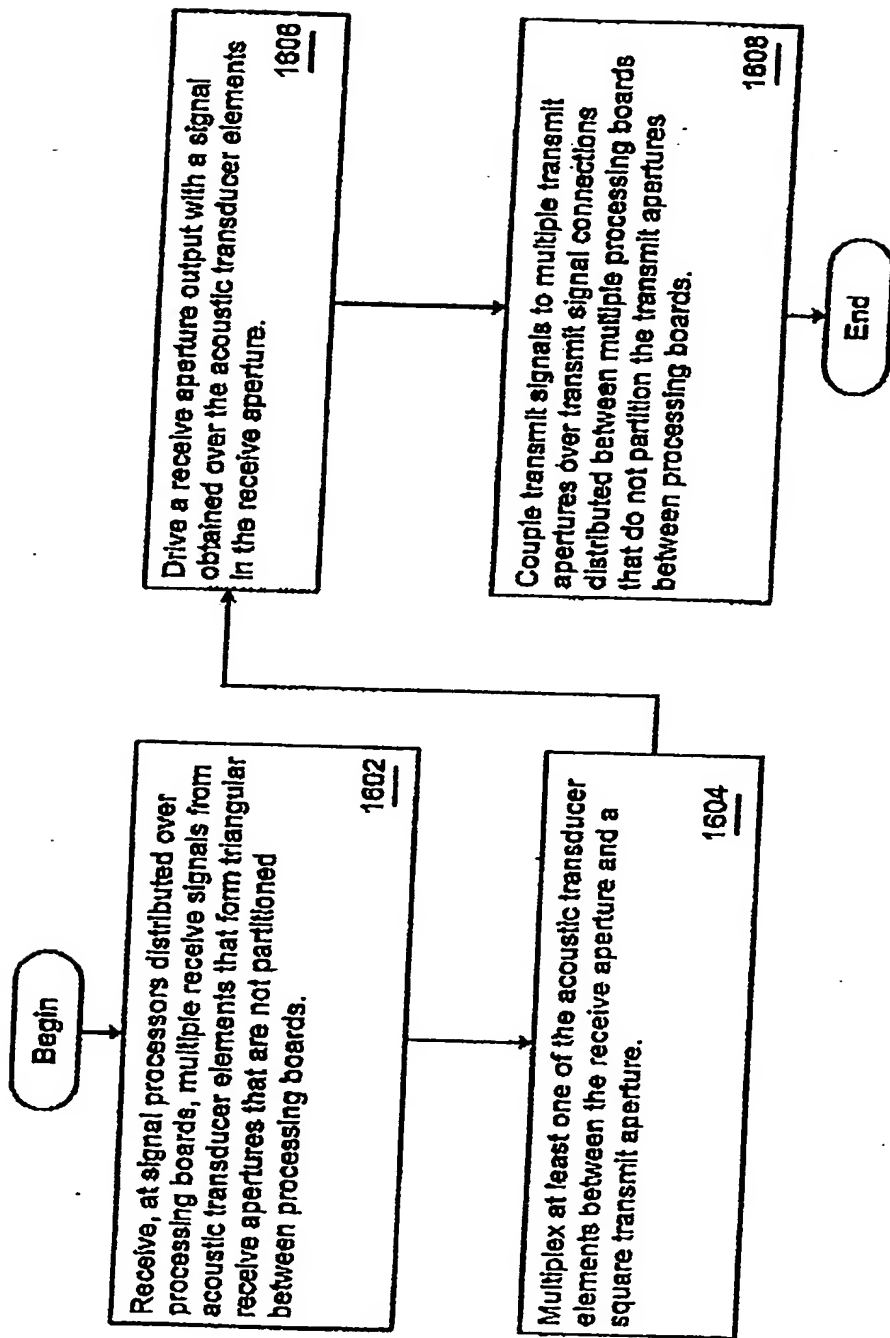
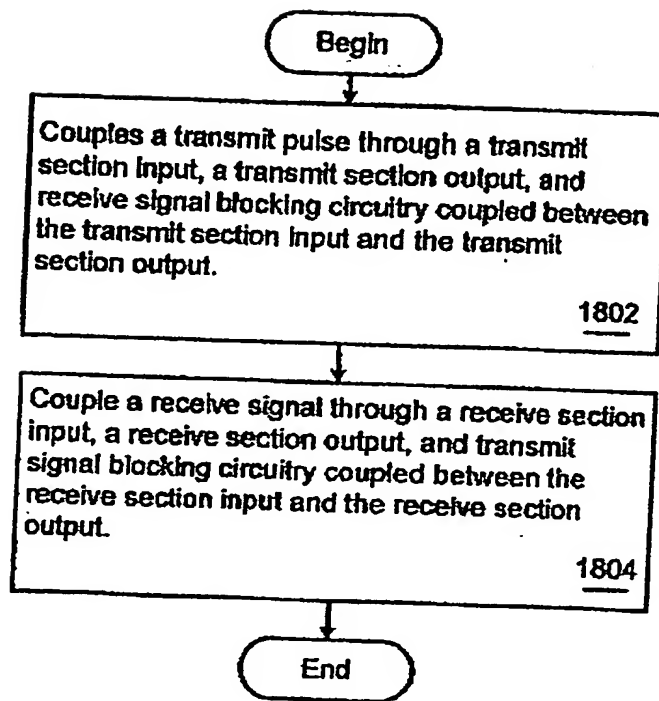


Figure 16

FIGURE 17

Begin
Receive directional parameters for received sub-apertures from a host system at cache memory controller. <u>1702</u>
Transfer the directional parameters to multiple signal processors on multiple processing boards. <u>1704</u>
Couple, to a first signal processor, receive signals arising form a receive sub-aperture. <u>1706</u>
Retrieve, from a cache memory, directional parameters for the receive sub-aperture. <u>1708</u>
Determine a beamforming delay derived from the directional parameters for the transducer elements in the receive sub-aperture. <u>1710</u>
Apply the delay to the receive signal from teach respective transducer element. <u>1712</u>
End



1800

Figure 18

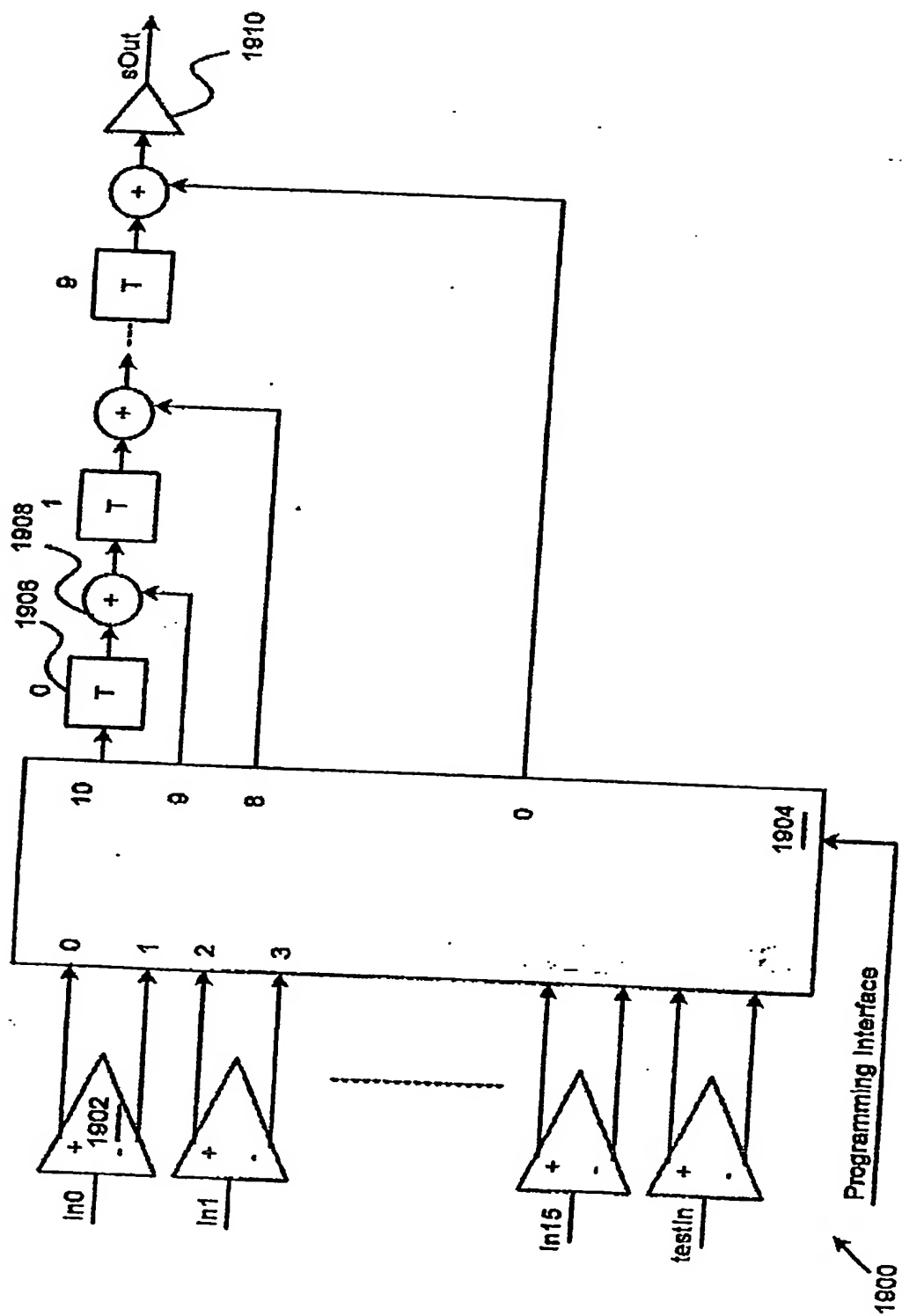


Figure 19